Serial No.: To Be Assigned Filed: Herewith

For: HIGH-K GATE DIELECTRIC STACK WITH BUFFER LAYER TO IMPROVE THRESHOLD VOLTAGE CHARACTERISTICS

Attorney Doc. No.: 67,200-1258

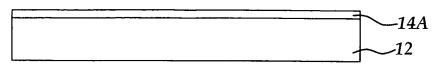


Figure 1A

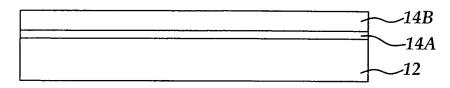


Figure 1B

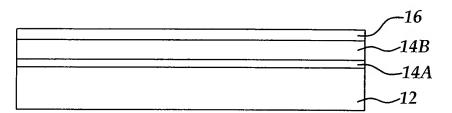


Figure 1C

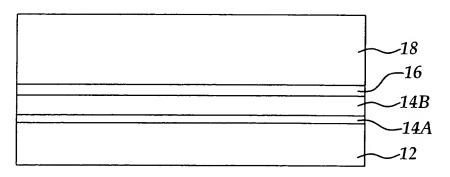


Figure 1D

1/2

Inventor: SHANG-CHIH CHEN
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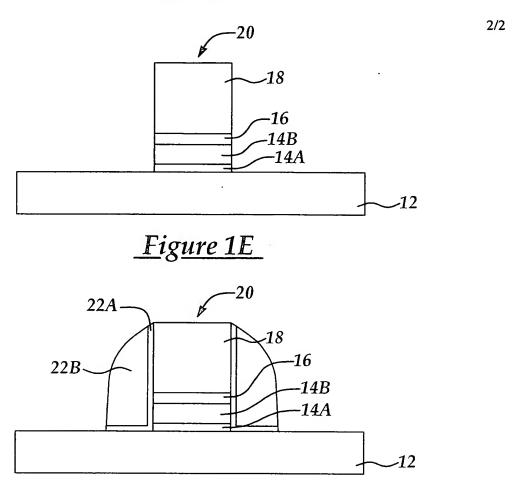
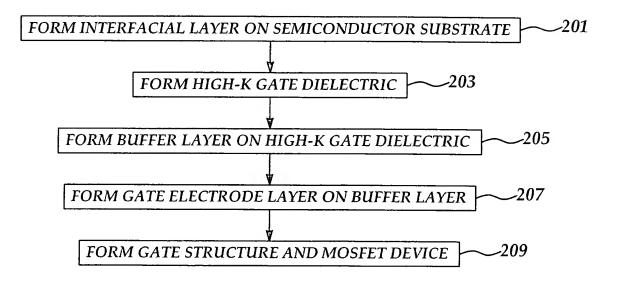


Figure 1F



<u>Figure 2</u>